

FIG.4

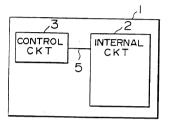


FIG.I

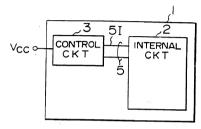
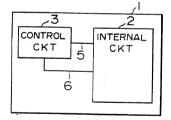


FIG.2

FIG.5



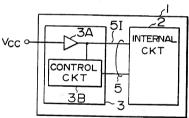


FIG.3

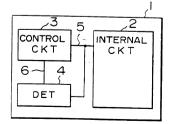
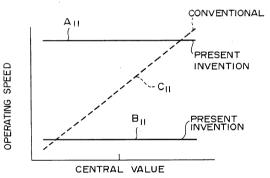


FIG.6



FABRICATION PROCESS CONDITION OR OPERATING CONDITION

FIG.7A

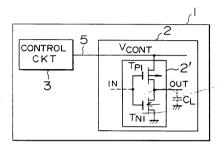
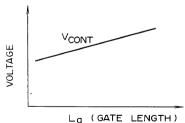


FIG.7B



VT (THRESHOLD VOLTAGE)

tox (THICKNESS OF GATE OXIDE FILM)

1/Bo(CHANNEL CONDUCTANCE)

(TEMPERATURE)

CL (CAPACITANCE)

FIG. 8

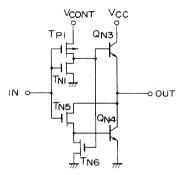


FIG. 9

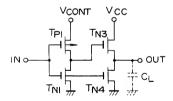


FIG. 10

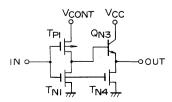


FIG.IIA

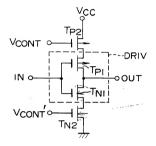


FIG. IIB

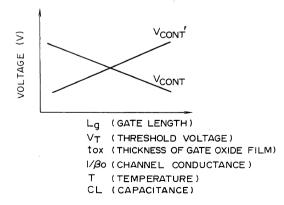


FIG.12A

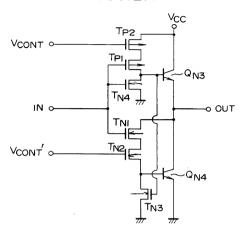


FIG. 12 B

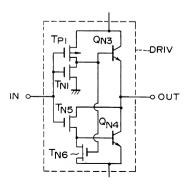


FIG.13

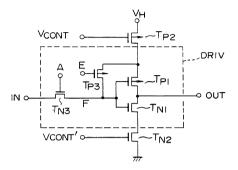
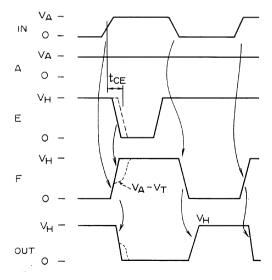


FIG.14



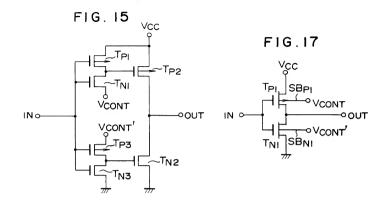
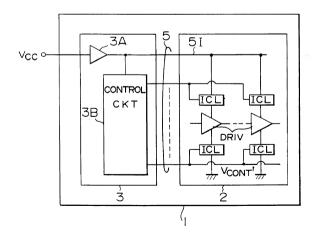


FIG.16



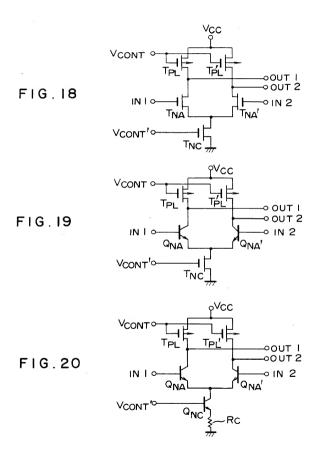


FIG. 21

FIG. 22

FIG. 23

VCC

TPR

SOLUTION

TOTAL

TOTA

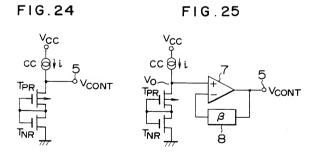
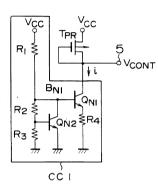


FIG. 26

FIG. 27



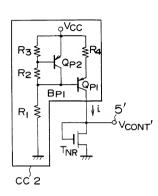
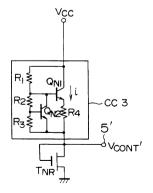


FIG. 28

FIG. 29



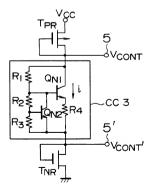


FIG. 30

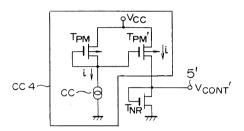


FIG. 31

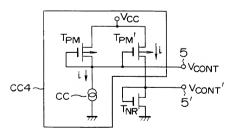


FIG. 32

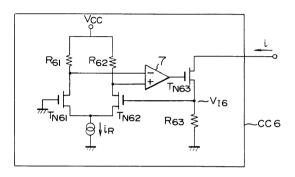


FIG.33

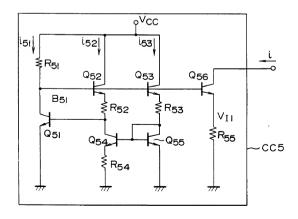


FIG. 34

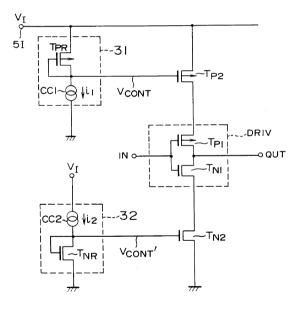


FIG.35

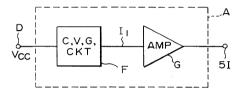


FIG.36

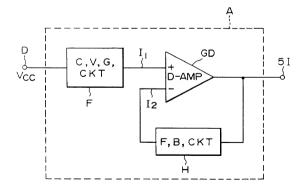


FIG.37

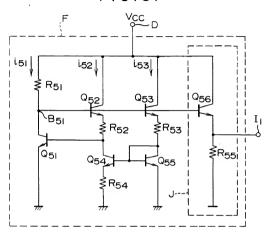


FIG.38

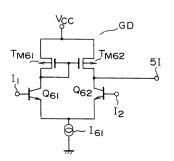


FIG.39

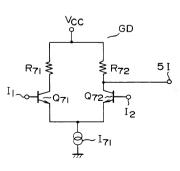


FIG. 40 FIG.41 √cc Усс GD GD TM61 Тм62 R₇₁≷ έR₇₂ Q61 Q62 Q71 Í2 T₁₆1 T_{I71}

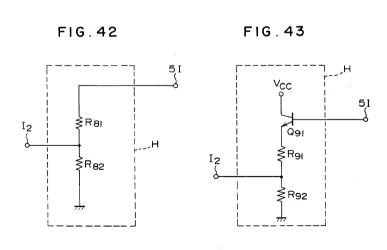




FIG.44

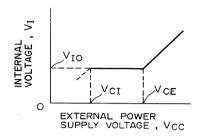


FIG.45

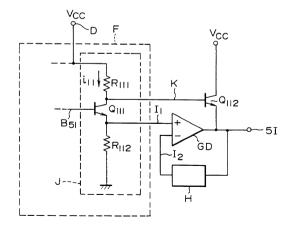


FIG.46

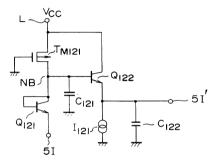
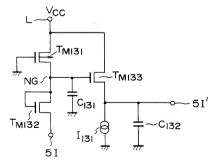


FIG. 47



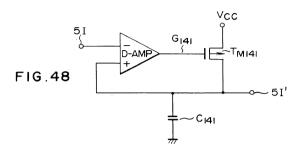


FIG. 49

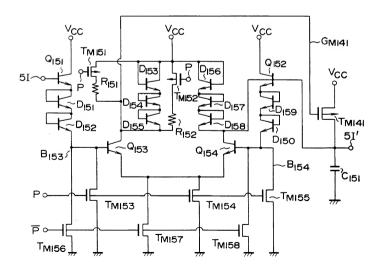


FIG.50A

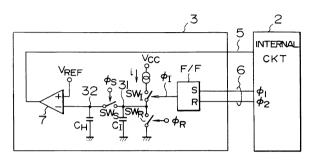
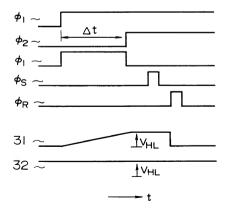


FIG.50B



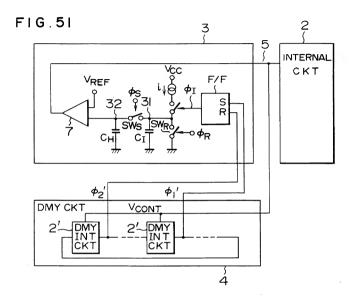


FIG.52

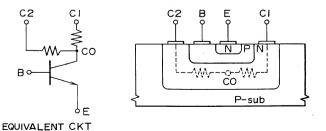


FIG.53

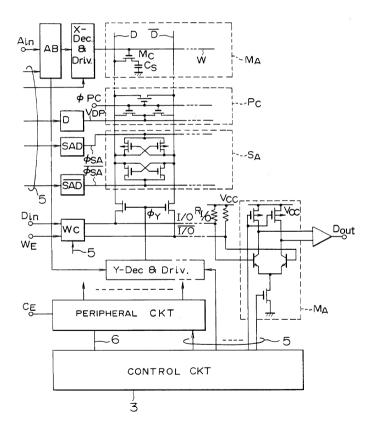


FIG.54

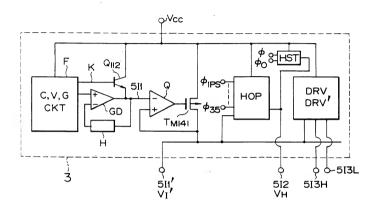


FIG.55

FIG. 56

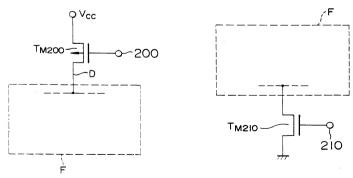


FIG.57

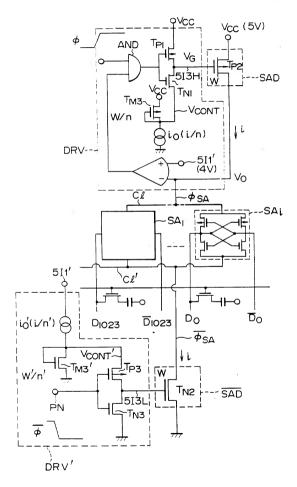


FIG.58

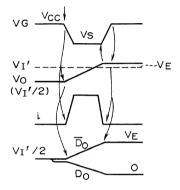


FIG.59

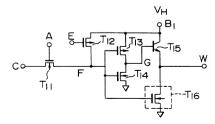


FIG. 60

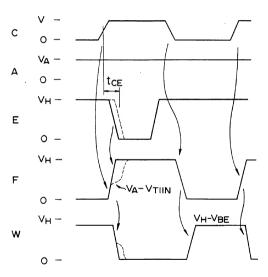


FIG. 61

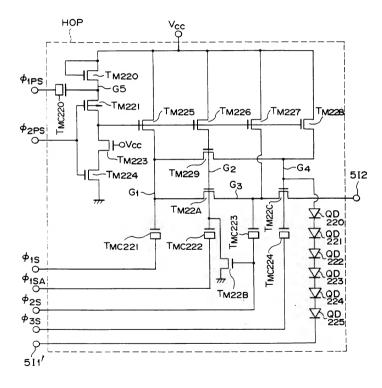
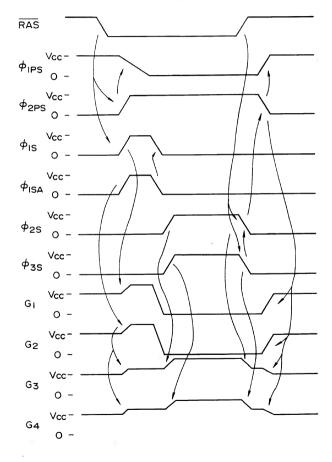


FIG.62



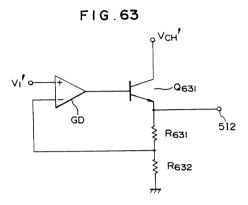


FIG. 66

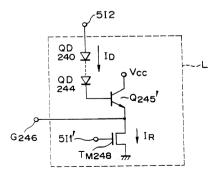


FIG.64

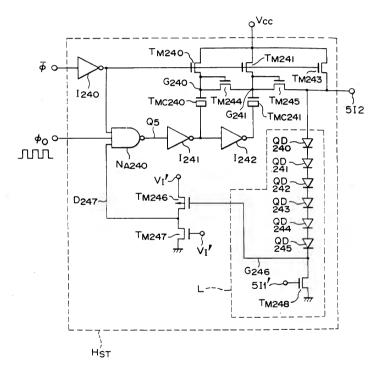
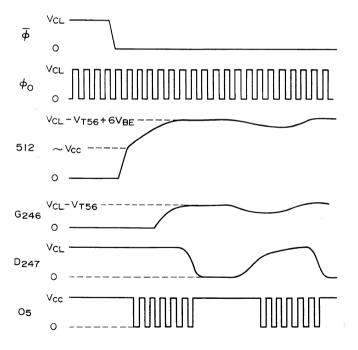


FIG. 65



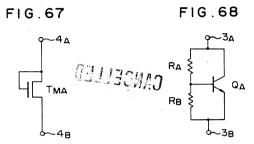


FIG.69

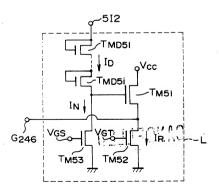


FIG.70

